CUSTOMER APPROVAL SHEET

Company Name	
MODEL	C090EAN04.1
CUSTOMER APPROVED	Title : Name :

- ☐ **APPROVAL FOR SPECIFICATIONS ONLY (Spec. Ver. 0.0)**
- ☐ **APPROVAL FOR SPECIFICATIONS AND ES SAMPLE (Spec. Ver. 0.0)**
- ☐ **APPROVAL FOR SPECIFICATIONS AND CS SAMPLE (Spec. Ver. 0.0)**
- ☐ **CUSTOMER REMARK :**

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Doc. Version	1
Total Page	24
Date	2018/8/30

Product Specification

9.0” COLOR TFT-LCD MODULE

MODEL NAME: C090EAN04.1

< ◆ >Preliminary Specification

< >Final Specification

Note: The content of this specification is subject to change.

Record of Revision

Version	Revise Date	Page	Content
1	2018/8/30	All	First draft.

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Contents

A. General Description

C090EAN04.1 is a a-Si type Thin Film Transistor Liquid crystal Display (TFT-LCD) with AHVA (Advanced Hyper-Viewing Angle) technology. This model is composed of a TFT-LCD, driver ICs, FPC (flexible printed circuit), and a backlight unit.

B. Features

- 9.0”-inch display
- 1280 x 720 RGB resolution in RGB stripe dot arrangement
- High brightness
- Interfaces: LVDS (8bit JEIDA, DE mode)
- AHVA – wide view technology
- RoHS compliance

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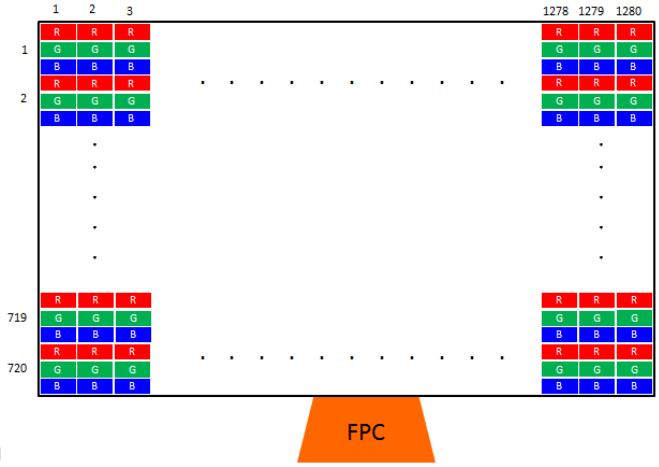
C. Physical Specifications

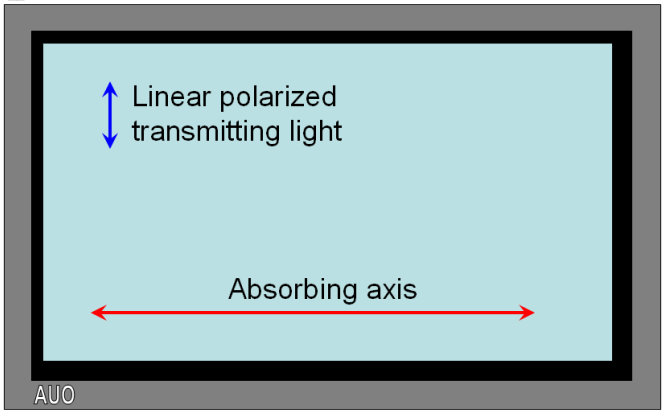
1. TFT LCD Panel

NO.	Item	Unit	Specification	Remark
1	Display Resolution	dot	1280 (H)x 720(V) RGB	
2	Active Area	mm	198.72 (H) x 111.78 (V)	
3	Screen Size	inch	9.0(Diagonal)	
4	Dot Pitch	μm	51.75 (R.G.B) x 155.25 (V)	
5	Color Configuration	–	R. G. B. Stripe	
6	Color Depth	–	16.7 M colors	
7	Overall Dimension	mm	212.2x127.9x7.6	w/o Boss
8	Weight	g	TBD+/- 10%	
9	Display Mode	–	Normally Black	
10	Surface Treatment	–	AG	

Note 1: Below figure shows dot stripe arrangement.



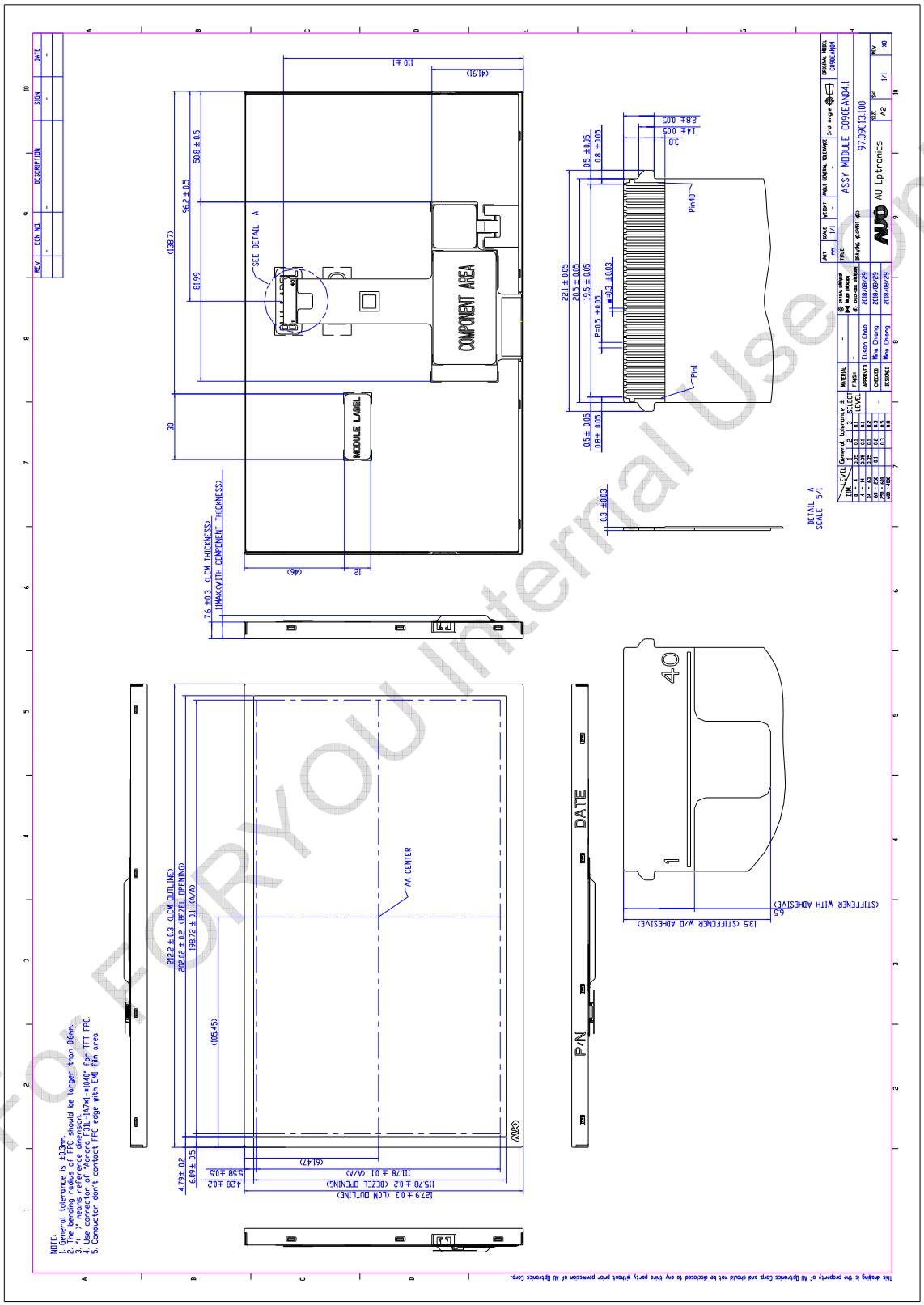
Note 2: Below figure shows dot stripe arrangement.



D. Outline Dimension

1. TFT-LCD Module

(Drawing is temporary, Need further discussion with Tier1)



E. Electrical Specifications

1. TFT LCD Panel Pin Assignment

Recommended Connector:

No.	Pin Name	I/O	Description	Remarks
1	GND	G	Ground.	
2	GND	G	Ground.	
3	PIND3	I	Positive LVDS differential input.	
4	NIND3	I	Negative LVDS differential input.	
5	GND	G	Ground.	
6	CLKP	I	Positive LVDS differential clock input.	
7	CLKN	I	Negative LVDS differential clock input.	
8	GND	G	Ground.	
9	PIND2	I	Positive LVDS differential input.	
10	NIND2	I	Negative LVDS differential input.	
11	GND	G	Ground.	
12	PIND1	I	Positive LVDS differential input.	
13	NIND1	I	Negative LVDS differential input.	
14	GND	G	Ground.	
15	PIND0	I	Positive LVDS differential input.	
16	NIND0	I	Negative LVDS differential input.	
17	GND	G	Ground.	
18	NC/CS		Dummy	NC,AUO test pin
19	NC/SCL		Dummy	NC,AUO test pin
20	NC/SDO		Dummy	NC,AUO test pin
21	NC/SDI		Dummy	NC,AUO test pin
22	GND	G	Ground.	
23	NC		Dummy	
24	VDD	PI	Digital power supply voltage.	
25	VDD	PI	Digital power supply voltage.	
26	VDD	PI	Digital power supply voltage.	
27	VDD	PI	Digital power supply voltage.	
28	NC/VPP		Dummy	NC,AUO test pin
29	GND	G	Ground.	
30	GND	G	Ground.	
31	TH-		Thermistor-	
32	TH+		Thermistor+	
33	CATHODE1		Power Supply for LED circuit(CATHODE1)	
34	CATHODE2		Power Supply for LED circuit(CATHODE2)	

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35	CATHODE3		Power Supply for LED circuit(CATHODE3)	
36	NC		Dummy	
37	NC		Dummy	
38	ANODE1		Power Supply for LED circuit(ATHODE1)	
39	ANODE2		Power Supply for LED circuit(ATHODE2)	
40	ANODE3		Power Supply for LED circuit(ATHODE3)	

I: Digital signal input, O: Digital signal output, G: GND, PI: Power input

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2. Differential Input Data Format

a. JEIDA format(DE Mode)

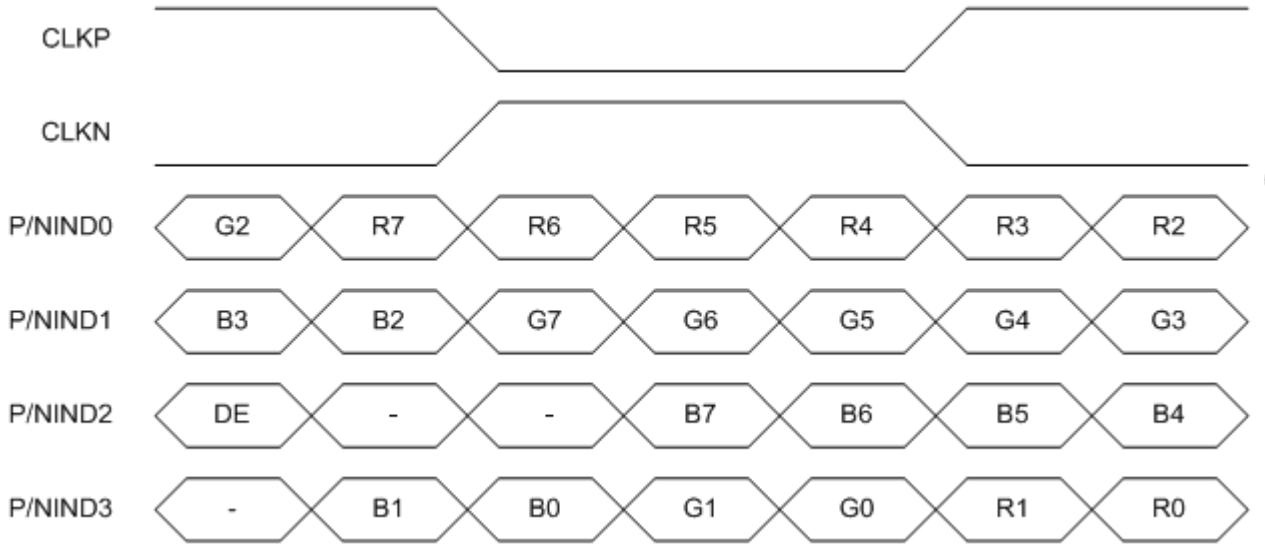


Fig. 1. LVDS input data JEIDA format

3. Input Timing Diagram

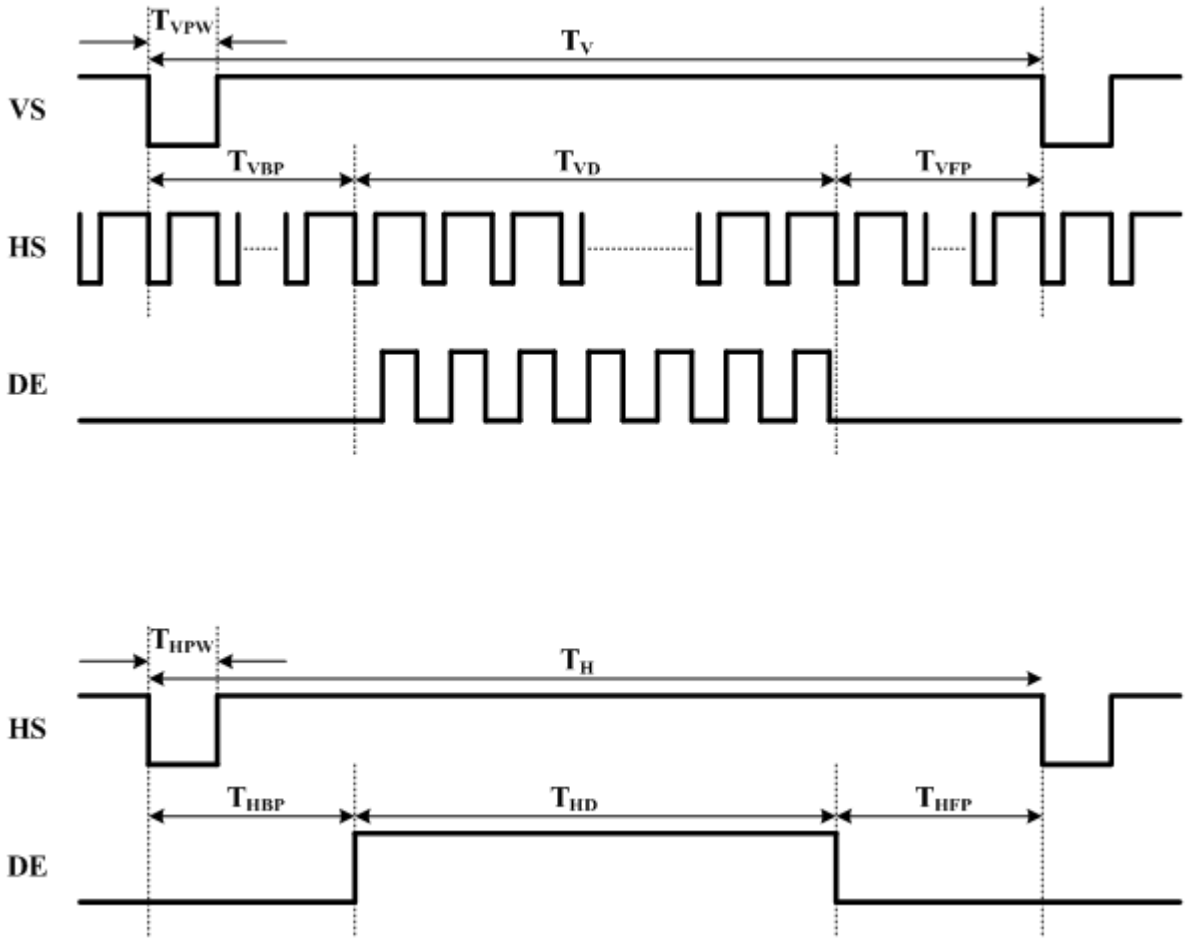


Fig. 2. Input Timing Diagram

DE Mode

Parameter		Symbol	Min.	Typ.	Max.	Unit.	Remark
CLK Frequency		F_{CLK}	66.03	71.15	80.08	MHz	
HSYNC	**Period**	T_H	1448	1540	1612	CLK	
	Horizontal display area	T_{HD}		1280		CLK	
	Blanking	$T_{HBP} + T_{HFP}$	168	260	332	CLK	
VSYNC	**Period**	T_V	760	770	828	HS	
	Vertical display area	T_{VD}		720		HS	
	Blanking	$T_{VBP} + T_{VFP}$	40	50	108	HS	

4. Absolute Maximum Ratings

Items	Symbol	Values		Unit	Condition
		Min.	Max.		
Power Voltage	VDD	-0.5	5	V	GND = 0V
Operation Temperature	Topa	-30	85	℃	Ambient
Storage Temperature	Tstg	-40	95	℃	Ambient

Note:Functional operation should be restricted under normal ambient temperature.

5. DC Electrical Characteristics

The following items are measured under stable condition and suggested application circuit.

a. Power Specification

Parameter	Symbol	Min	Typ	Max	Unit	Notes
Power Supply	VDD	3.0	3.3	3.6	V	
	IVDD	-	128.3	312	mA	Note 3
	Inrush IVDD	-	-	451	mA	Note 4

Note 1: All conditions should be set typical value
Note 2: The panel can operate normally in the recommended operating condition.
Note 3: Test pattern is as the following picture..

Fig1. Typical current situation

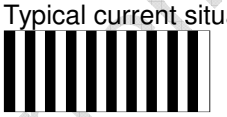
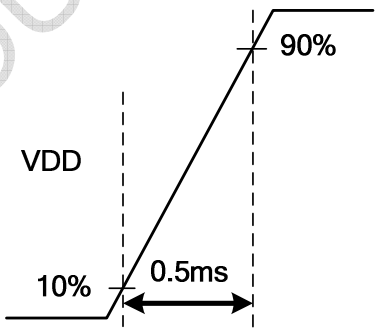


Fig2. Max current situation: Vertical stripe pattern alternating 0 gray scale with 255 gray scale every dot

Note 4: Test condition is the VDD voltage range between 3.3V on the rising time 0.5ms,
Please see below picture.



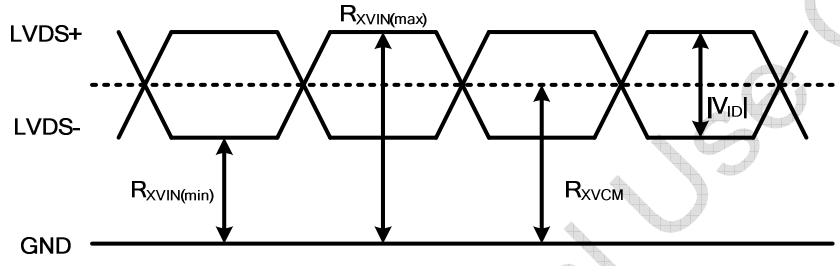
a. Signal DC Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Notes
Input signal voltage	Vi	-0.3	-	VDD	V	Note1
Input high level voltage	V_{IH}	0.7VDD	-	VDD	V	Note1
Input low level voltage	V_{IL}	GND	-	0.3VDD	V	Note1
Differential input high threshold	R_{XVTH}	0.2	-	-	V	Note 2

Differential input low threshold	R_{XVTL}	-	-	-0.2	V	Note 2
Input voltage range (singled-end)	R_{XVIN}	0	-	(VDD-1.2)	V	Note 2
Input differential voltage	$\|V_{ID}\|$	0.15	-	0.6	V	Note 2
Differential Input Common Mode Voltage	R_{XVCM}	|VID|/2	-	(VDD-1.2 -|VID|/2)	V	Note 2

Note 1: TTL interface signal DC characteristics
Note 2: LVDS interface signal DC characteristic

Single-end Signal



Differential Signal

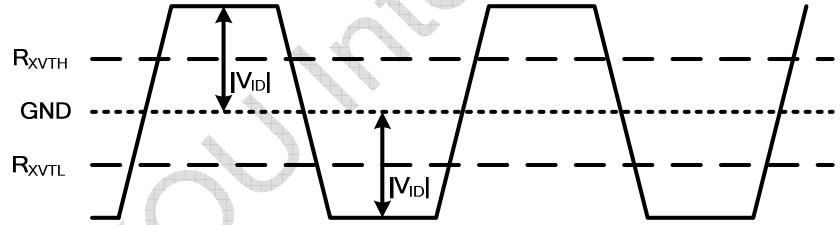


Fig. 3. LVDS DC characteristics diagram

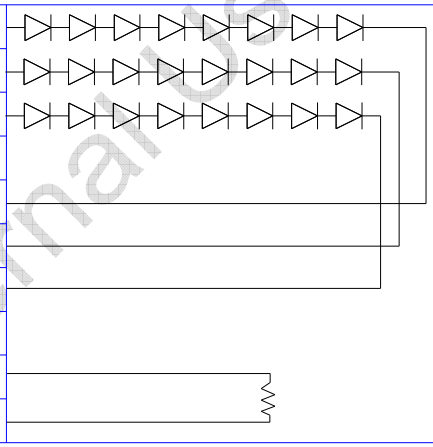
b. Backlight Driving Conditions (Note 1)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED Supply Current	I_L		80	85	mA	single serial (Note 3)
LED Supply Voltage	V_L			27.2	V	single serial (Note 3)
LED Life Time	L_L	10,000			hr	Note 2

Note 1: light-bar has 24 pieces of LED (3 strings, 8 pieces for each string).
Note 2: LED life time defining the 50% decreasing of the original brightness is 10,000 hours under the 80 mA of LED current in 25 ℃..
Note 3: The LED supply power is for 3 string of LED.
Note 4: The voltage capacity of LED driver IC must be over max. of LED Voltage.

Fig. 4. Light bar structure

BACK LIGHT FPC PIN ASSIGMENT		
PIN1	LED 1,4,7,10,13,16,19,22	+1
PIN2	LED 2,5,8,11,14,17,20,23	+2
PIN3	LED 3,6,9,12,15,18,21,24	+3
PIN4	NA	
PIN5	CATHODE	-1
PIN6	CATHODE	-2
PIN7	CATHODE	-3
PIN8	NA	
PIN9	NTC+	+
PIN10	NTC-	-



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6. AC Electrical Characteristics

a. Input AC characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
VDD power on slew time	T_{POR}	1	-	20	ms	From 0V to 90% VDD

Fig. 6. VDD timing diagram

b. Differential signal AC characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Clock frequency	R_{XFCLK}	66.03	71.15	80.08	MHz	
Input data skew margin	T_{RSKM}			400	ps	

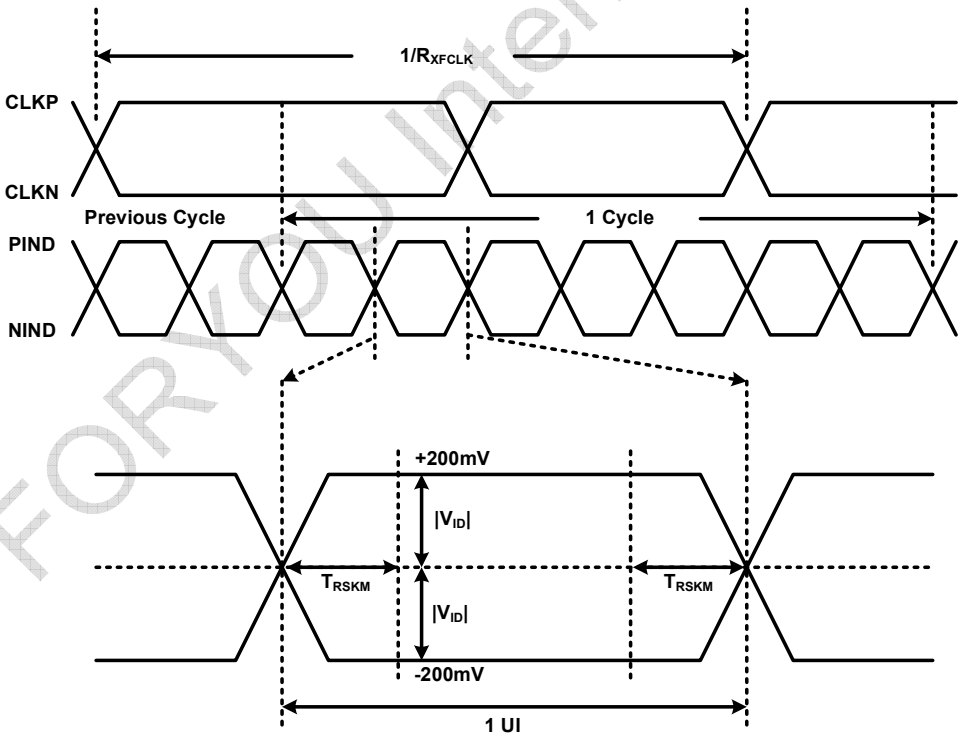

Fig. 7 LVDS AC characteristics diagram

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8. Power on/off sequence

The LCD adopts high voltage driver IC, so it could be permanently damaged under a wrong power on/off sequence. The suggested LCD power sequence is below:

a. Power on sequence:

VDD →LVDS → BKLEN

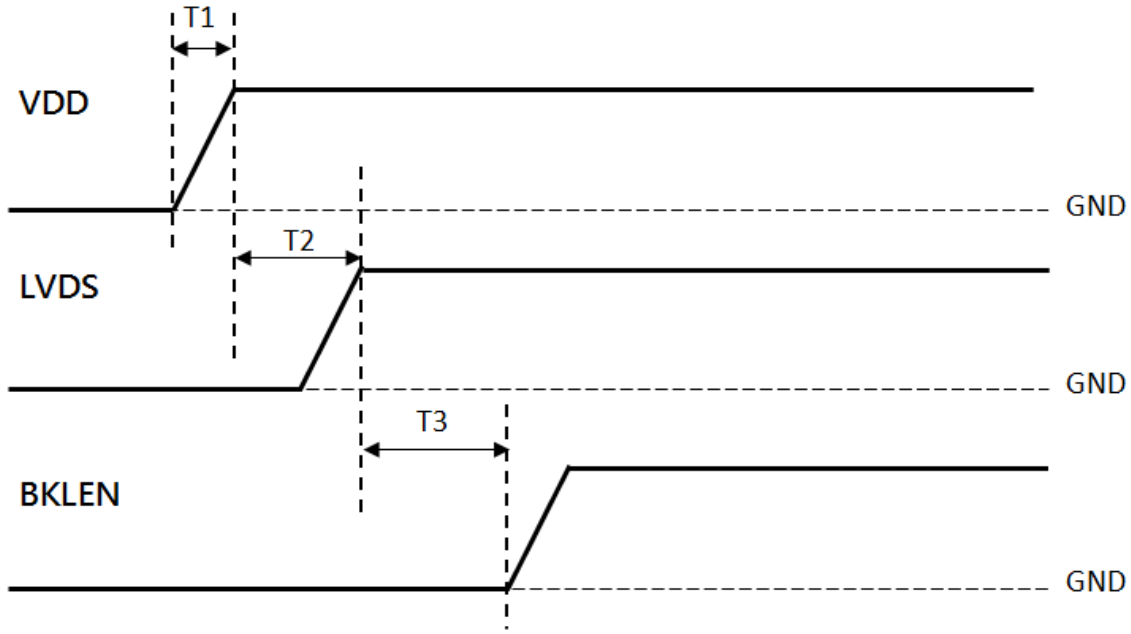


Fig. 8. Power on sequence (GND=0V)

Power on timing:

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	1	--	20	ms
T2	1	--	10	ms
T3	300	350	--	ms

b. Power Off sequence:

BKLEN → LVDS → VDD

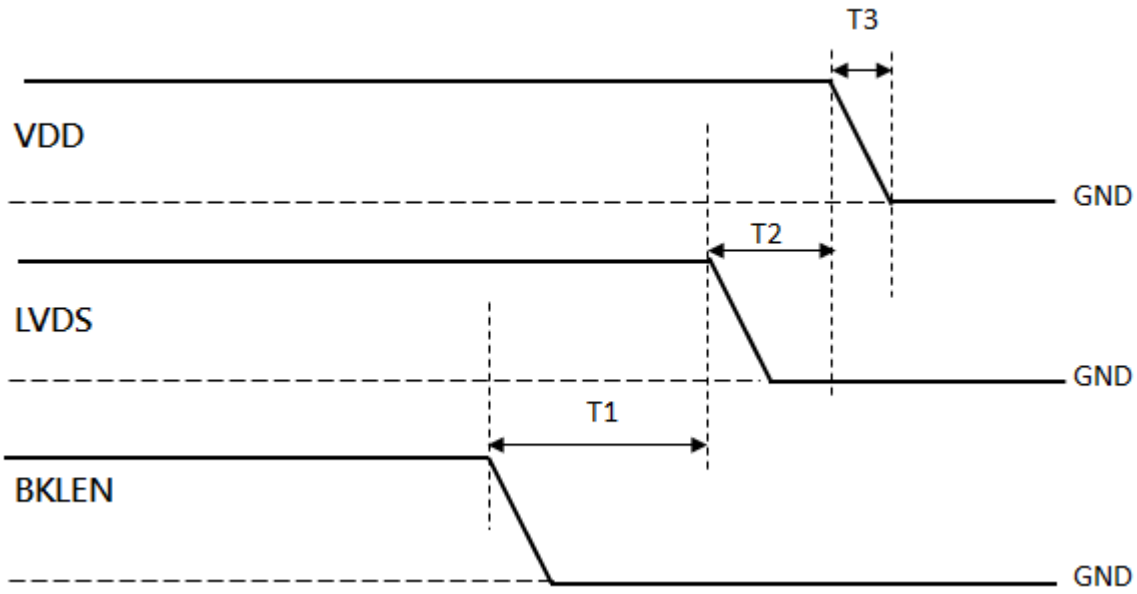


Fig. 9. Power off sequence (GND=0V)

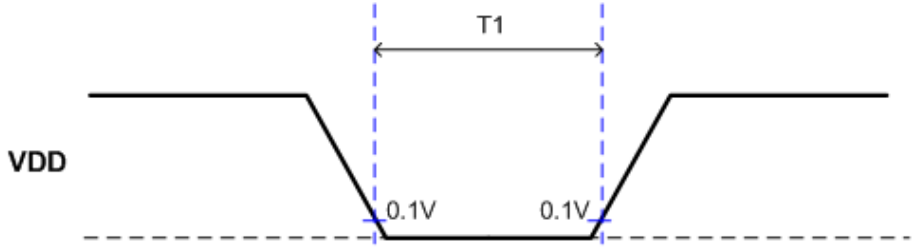
Power off timing:

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	90	100	--	ms
T2	1	--	10	ms
T3	1	--	10	ms

c. VDD off to on timing

Parameter	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	1		--	s	Note 1

Note 1：Before VDD turns ON, please make sure that AVDD; VGH; VGL; are all below intensity of 0.1V.



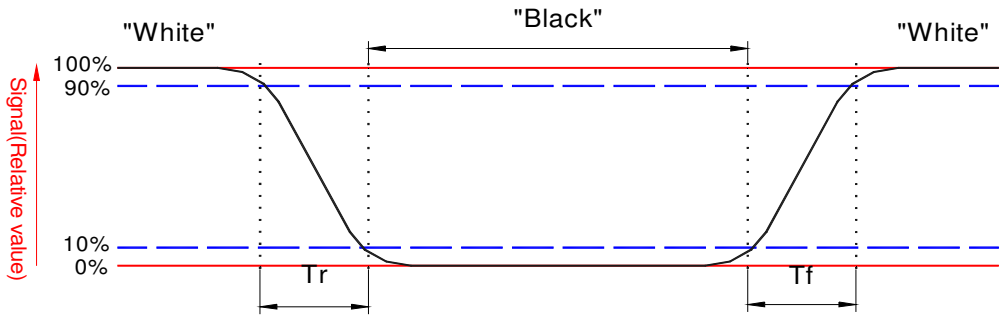
F. Optical specifications (Note 1, 2)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Response time (Rise + Fall)	Tr + Tf	θ=0° at 25℃(Ta)	-	30	40	ms	Note 3
		θ=0° at 0℃(Ta)	-	65	95	ms	
		θ=0° at -20℃(Ta)	-	150	200	ms	
		θ=0° at -30℃(Ta)	-	300	450	ms	
Viewing Angle Top Bottom Left Right	*θ*	CR ≧ 10	 70 70 70 70	 80 80 80 80	-	deg.	Note 7, 8
Contrast ratio	CR1	θ=0°	700	1000	-		Note 4, 5, 6
Brightness	Y_L	θ=0°	(770)	(900)	-	cd/m^2	Note 1,2,9
White Chromaticity	X	θ = 0°	0.256	0.296	0.336		Note 10
	Y	θ = 0°	0.273	0.313	0.353		
Red Chromaticity	X	θ = 0°	0.600	0.640	0.680		
	Y	θ = 0°	0.293	0.333	0.373		
Green Chromaticity	X	θ = 0°	0.258	0.298	0.338		
	Y	θ = 0°	0.562	0.602	0.642		
Blue Chromaticity	X	θ = 0°	0.108	0.148	0.188		
	Y	θ=0°	0.018	0.058	0.098		
Uniformity		θ=0°	70			%	Note 11
NTSC				70		%	
Surface reflectance	Rfsurf(SCI)	θ=0°			4.5	%	Note 12

Note 1: Measurement should be performed in the dark room, optical ambient temperature = 25 ℃, and backlight current I_L = 80mA.

Note 2: To be measured in the center area of TFT-LCD with a field angle of 1° by Topcon luminance meter SR3, after 10 minutes operation and warm up 30 minutes.

Note 3: Definition of response time:
The output signals of photo detector are measured when the input signals are changed from “black state” to “white state” (falling time) and from “white state” to “black state” (rising time), respectively.



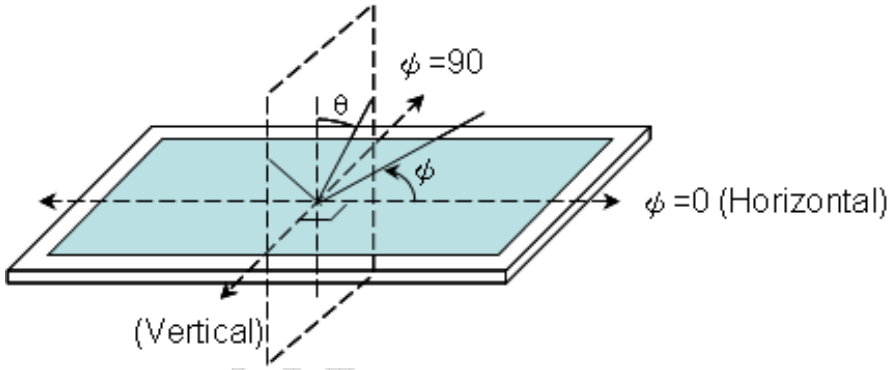
Note 4: Based on liquid crystal characteristics, the response time will become slower and the color of panel will become darker than the above optical specification when ambient temperature is below 25 ℃.

Note 5: Contrast ratio is calculated with the following formula.

$$\text{Contrastratio} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

Note 6: When "White" state, R[7:0]=G[7:0]=B[7:0]=11111111
When "Black" state, R[7:0]=G[7:0]=B[7:0]=0000000

Note 7: Definition of viewing angle: refer to figure as below.

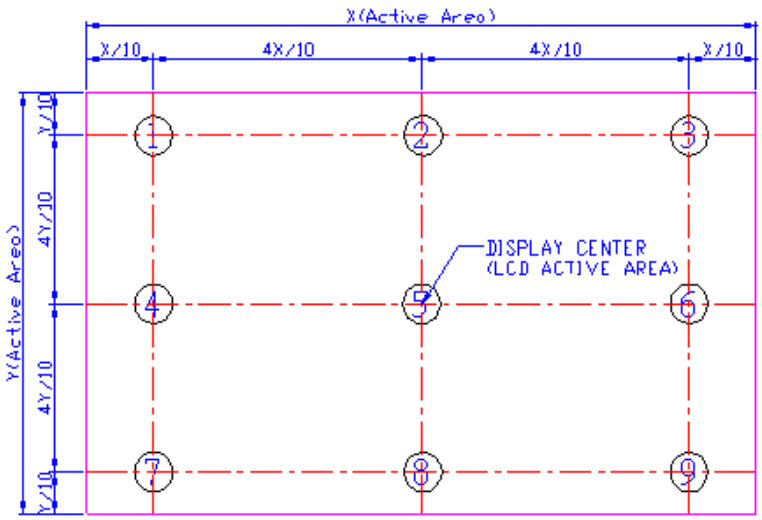


Note 8: Viewing angles are measured at the center of the panel when all the input terminals of LCD oanel are electrically opened.

Note 9: Brightness is measured at the center of the display.

Note 10: The viewing angles are measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 11: Luminance Uniformity of these 9 points is defined as below: (1 : 4 : 4 : 1)



$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$$

Note 12.

Measured on the center area of the LCD active area .

Measure equipment: SR3 or SR3A

G. Reliability Test Items(Note 1~3)

No.	Test items	Conditions		Remark
1	High temperature storage	Ta= 95 ℃	500 Hrs	
2	Low temperature storage	Ta= -40 ℃	500 Hrs	
3	High temperature operation	Ta= 85 ℃	500 Hrs	
4	Low temperature operation	Ta= -30 ℃	500 Hrs	
5	High temperature and high humidity	Ta= 60 ℃, 90 % RH	500 Hrs	Operation
6	Heat shock	-30 ℃ ~ 85 ℃ / 200 cycles 1 Hrs/cycle		Non-operation
7	Shock	100 G, 6 ms, ±X, ±Y, ±Z 3 times for each direction		
8	Electrostatic Discharge	Contact = ± 8 kV, class B Air = ± 15 kV, class B		IEC61000-4-2
9	Vibration	Frequency range	8 ~ 33.3 Hz	JIS D1601,A10 Condition A
		Stoke	1.3 mm	
		Sweep	2.9 G, 33.3 ~ 400Hz	
		Cycle	15 min.	
		2 hours for each direction of X, Z 4 hours for Y direction		
10	Vibration (with carton)	Random vibration: 0.015 G^2/Hz from 5 ~ 200 Hz –6 dB/Octave from 200 ~ 500 Hz		IEC 68-34
11	Drop (with carton)	Height: 60 cm 1 corner, 3 edges, 6 surfaces		

Note 1: Ta: Ambient temperature
Note 2: In the standard condition, there is no display function NG issue occurred. All the cosmetic specification is judged before the reliability stress.
Note 3: I_L = 80mA

H. Quality Spec

NO.	Inspection Item	Inspection Standards
1	Black spots	φ≤0.10 0.10 < φ ≤ 0.3 0.3< φ
2	White spots	φ≤0.10 0.10 < φ ≤ 0.3 0.3< φ
3	Bright pixel dot	1 dot R&G&B&W by human eye
4	Dark pixel dot	1 dot by human eye
5	Lines &Scratches	W≤0.03 and L≤1.0 0.03< W≤0.05 and L≤3.0 0.05< W or L>3.0 Visible by human eye
6	Dent/Bubble	φ≤0.10 0.10 < φ ≤ 0.3 0.30< φ Visible by human eye
7	Mura	Visible by human eye at full black/white pattern Visible by 5% ND filter

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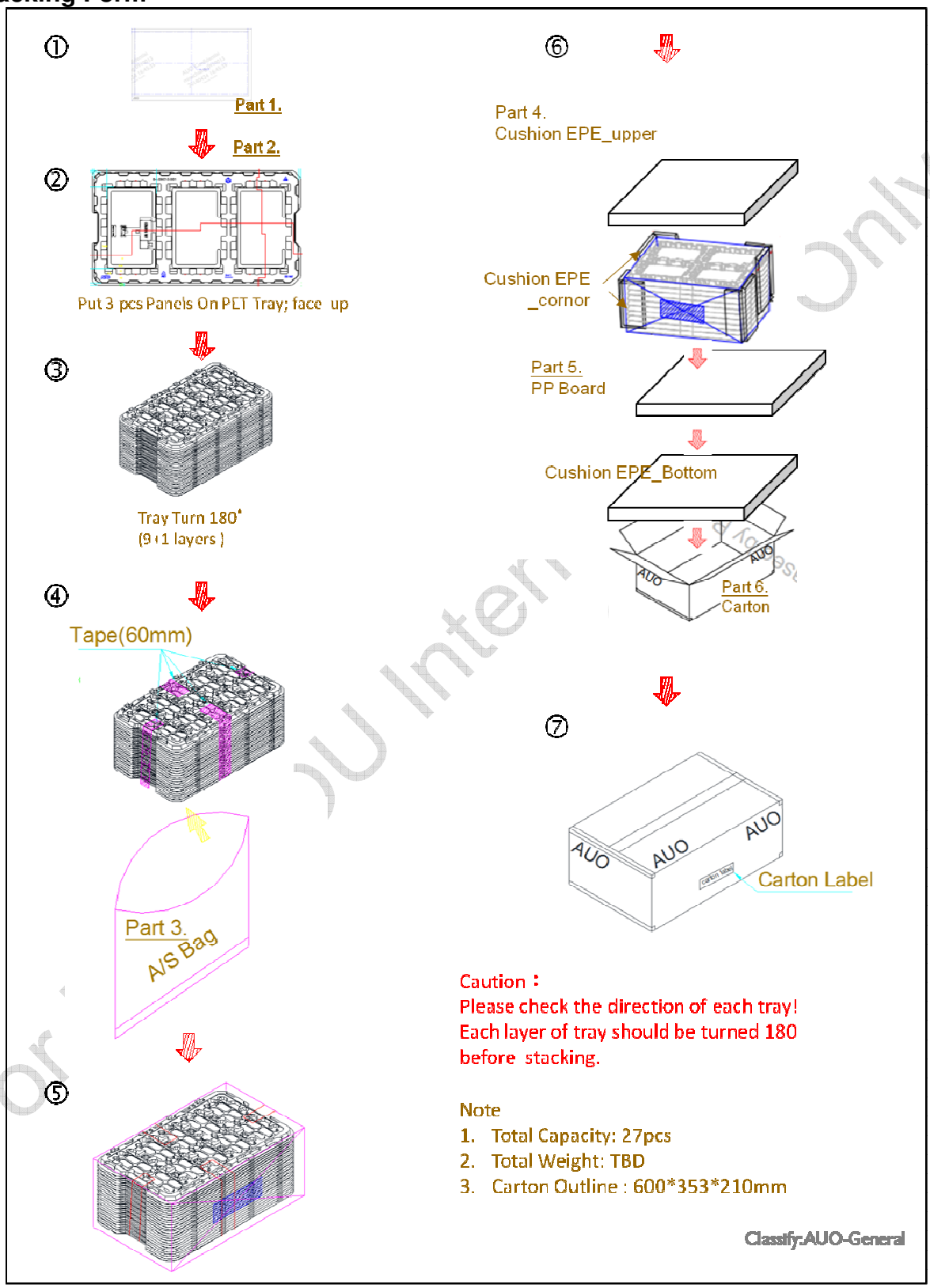
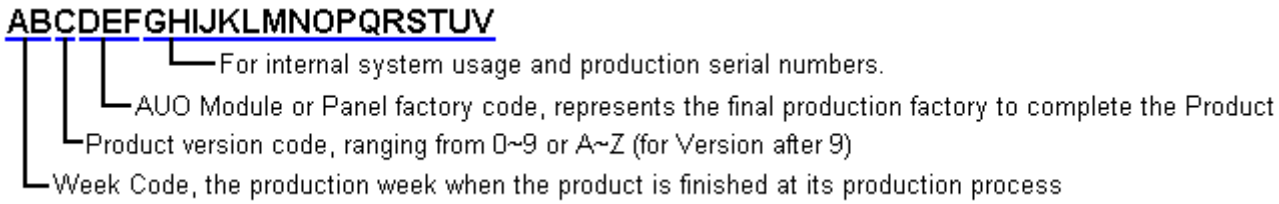
I. Packing and Marking

1. Packing Form



2. Module/Panel Label Information

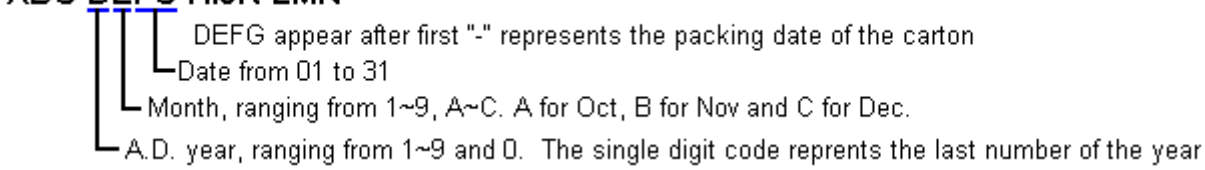
The module/panel (collectively called as the “Product”) will be attached with a label of Shipping Number which represents the identification of the Product at a specific location. Refer to the Product outline drawing for detailed location and size of the label. The label is composed of a 22-digit serial number with the following definition:



Example:
501M06ZL06123456781Z05:
Product Manufacturing Week Code: WK50
Product Version: Version 1
Product Manufacturing Factory: M06

3. Carton Label Information

The packing carton will be attached with a carton label where packing Q’ty, AUO Model Name, AUO Part Number, Customer Part Number (Optional) and a series of Carton Number in 13 or 14 digits are printed. The Carton Number is appearing in the following format:



Refer to the drawing of packing format for the location and size of the carton label.